Overhead for Dynamic Reconfig: 24 cycles = 12 UART + 12 SPI

(0021) DCB03FN = 0x0D; //set as transmit UART

0627: 71 10 OR F,0x10

0629: 62 2C 0D MOV REG[0x2C],0xD

(0046) DCB03FN = 0x06; //set as master SPI

067C: 71 10 OR F,0x10

067E: 62 2C 06 MOV REG[0x2C],0x6

Overhead for Data Communication: 93 UART

0654: 62 D0 00 MOV REG[0xD0],0x0

0657: 55 13 07 MOV [\_\_r0],0x7

065A: 5A 12 MOV [\_\_r1],X

065C: 06 12 03 ADD [\_\_r1],0x3

065F: 52 02 MOV A,[X+2]

0661: 02 12 ADD A,[\_\_r1]

0663: 53 12 MOV [\_\_r1],A

0665: 52 01 MOV A,[X+1]

0667: 0A 13 ADC A,[\_\_r0]

0669: 60 D4 MOV REG[0xD4],A

066B: 3E 12 MVI A,[\_\_r1]

066D: 10 PUSH X

066E: 7C 04 46 LCALL \_UART\_SendData

0671: 20 POP X

Overhead for Data Communication: 73 UART

0699: 55 13 07 MOV [\_\_r0],0x7

069C: 5A 12 MOV [\_\_r1],X

069E: 06 12 03 ADD [\_\_r1],0x3

06A1: 52 02 MOV A,[X+2]

06A3: 02 12 ADD A,[\_\_r1]

06A5: 53 12 MOV [\_\_r1],A

06A7: 52 01 MOV A,[X+1]

06A9: 0A 13 ADC A,[\_\_r0]

06AB: 60 D4 MOV REG[0xD4],A

06AD: 3E 12 MVI A,[\_\_r1]

06AF: 10 PUSH X

06B0: 7C 06 12 LCALL \_SPIM\_SendTxData

06B3: 20 POP X

For UART starting : 98 cycles

Without considering Start/Stop for UART and SPI the overhead of dynamic configuration compared to the data communication execution time is 12/(93+12) for UART and 12/(73+12) for SPI. Calculating just the starting task of UART is 98 cycles alone, assuming SPI is almost the same, that means approx 98+12+stopping\_exec\_time is just overhead for dynamic configuration which is much more than the execution time for data transfers.